



**CALIFORNIA STATE SCIENCE FAIR  
2004 PROJECT SUMMARY**

<b>Name(s)</b> <b>Aditya Rajagopal</b>	<b>Project Number</b> <b>S0524</b>
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<b>Project Title</b> <b>Synthesis of Gallium Oxide Nanowires by Chemical Vapor Deposition</b>
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<p style="text-align: center;"><b>Abstract</b></p> <p><b>Objectives/Goals</b> The goal of this project was to synthesize gallium oxide nanowires.</p> <p><b>Methods/Materials</b> The fabrication of the nanowires was done by a process known as chemical vapor deposition. In this method, the gallium metal is vaporized in a vacuum tube, while an mixture argon-oxygen gas is flowed in. The oxygen reacts with the vaporized gallium to form a gallium(3+) oxide molecule. This gallium oxide molecule then deposits on a silicon substrate laced with a gold catalyst. The wires then grow radially outward from the single gold catalyst molecule. The stucture, composition, and presence of wires is then checked using a Scanning Electron Microscope.</p> <p><b>Results</b> In my project, I tested the effect of temperature fluctuation on the synthesis of gallium oxide, Ga<sub>2</sub>O<sub>3</sub>, nanowires. I attempted to synthesize wires at the following temperatures: 850 centigrade (C), 860 C, 870 C, 880 C, 920 C, 940 C, 950 C, and 980 C. During all the synthesis the pressure was kept approximately constant at 10<sup>-4</sup> torr; furthermore, the a constant flow-rate, constant reaction time, and similar silicon wafers assured that the differences in the synthesis processes would be due to differences in temperature alone.</p> <p>To that end, I observed a distinct difference between the synthesis of nanowires in the different temperature ranges. In the first synthesis at 920 C, I observed a plethora of nanowires of different sizes and lengths. Most wires appeared to be well over tens of microns in length. The diameter of the wires, which is controlled by the size of the gold catalyst molecules, ranged from 16.2nm to 34nm, with an average of 22.4nm.</p> <p>Furthermore, a large number of nanowires were also found on the samples fabricated at 940 C, 950 C, 880 C, and 870 C. The synthesis reactions at 980 C, 860 C, and 850 C yielded no wires.</p> <p><b>Conclusions/Discussion</b> I found empirically that the best heating temperature was 920 degrees; this temperature resulted in the largest number of actual wires (approximated), as well as the best quality of wires. The goal of this project was to discover if it was possible to grow gallium oxide nanowires, and if so, to find the best synthesis conditions for these wires. To this end, I was successful. The data I collected in this experiment serves as a foundation for my continuing research concerning the electrical properties of gallium oxide nanowires.</p>
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<b>Summary Statement</b> My project is about the synthesis of Gallium Oxide nanowires, possible semiconductors, for future use in nanodevices such as MEMs.
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<b>Help Received</b> Used lab equipment at UCI; Mr. Zhiyong Fan's help in the electron microscope imaging of the wires.
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