



**CALIFORNIA STATE SCIENCE FAIR  
2002 PROJECT SUMMARY**

<b>Name(s)</b> <b>Austin Clow; Nathan Poppelreiter; Nemo Smith</b>	<b>Project Number</b> <b>S0704</b>
<b>Project Title</b> <b>Take a R.I.S.C.</b>	
<p style="text-align: center;"><b>Abstract</b></p> <p><b>Objectives/Goals</b> We wanted to see if we could build a RISC (Reduced Instruction Set Computer), which is in the realm of current research.</p> <p><b>Methods/Materials</b> We used many standard and advanced Boolean Algebra techniques. It was critical that we researched each process for best results. We used microchips such as: adders, inverters, clocks, and tristate buffers; wires, and a proto-board.</p> <p><b>Results</b> We could build a Reduced Instruction Set Computer. In many stages of our debugging we had very low accuracy rates. We fixed these problems by researching more efficient ways of processing commands and eliminating all processes that were not needed.</p> <p><b>Conclusions/Discussion</b> We concluded that this can actually be done and is a legitimate and efficient way processing data.</p>	
<b>Summary Statement</b> Our project was about building a true R.I.S.C. processor.	
<b>Help Received</b>	